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PATENT
Serial No. 09/849,010



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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re Application of: Klevans et al.
Serial No.: 09/849,010
Filing Date: May 4, 2001
Title: METHOD AND SYSTEM FOR HARDWARE
SIMULATION

Honorable Assistant Commissioner
for Patents
Washington, D.C. 20231

Dear Sir:

I hereby certify that this correspondence is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" under 37 C.F.R. § 1.10 on the date indicated below and is addressed to: Assistant Commissioner for Patents, U.S. Patent and Trademark Office, P.O. Box 2327, Arlington, VA 22202.

Willie J. Jiles

Name

December 6, 2001
Date of Signature

Express Mail No. 501000354 US

TRANSMITTAL LETTER

Attached for filing in the above-identified patent application are the following:

1. Amendment and Request Pursuant to 37 C.F.R. § 1.48(a);
2. A check in the amount of \$130.00;
3. Consent of Assignee to Add an Inventor to Originally Named Inventor and Certificate Under 37 C.F.R. § 3.73(b) ;
4. Statement of Rajaraman Krishnan Under 37 C.F.R. § 1.48(a)(2);
5. Statement of Suresh Ramakrishnan Under 37 C.F.R. § 1.48(a)(2); and
6. Combined Declaration and Power of Attorney.

No other fees are believed to be due, however, the Commissioner is hereby authorized to charge any fees or credit any overpayments to Deposit Account No. 02-0384 of Baker Botts L.L.P.

Respectfully submitted,

BAKER BOTT S L.L.P.
Attorneys for Applicants

Barton E. Showalter
Reg. No. 38,302

2001 Ross Avenue
Dallas, Texas 75201-2980
(214) 953-6509

Date: Dec. 6, 2001

CORRESPONDENCE ADDRESS:

Baker Botts L.L.P.
2001 Ross Avenue, Suite 600
Dallas, TX 75201-2980